ched continuation page, I hereby declare that: As an inventor named below or on any

My residence, post office address and citizenship are as stated next to my name.

is attached hereto.

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled SPACER FOR SEMICONDUCTOR DEVICES, SEMICONDUCTOR DEVICES AND ASSEMBLIES INCLUDING THE SPACER, AND METHODS, the specification of which (check one):

s amended under PCT Article 19 on		
	 ,	
pecification, including the claims, as an	ended by any an	nendment
tion known to me to be material to the p degulations § 1.56.	atentability of the	subject
regulations § 1.56.		
or § 365(b) of any foreign application(ry other than the United States of Amer page any foreign application for patent States of America having a filing date be	ca listed below a	nd on any
	Priority Claim	ed
(day/month/year filed)	Yes	No
(day/month/year filed)	Yes	No No
	ling date of this	application:
(status - pending, patented	mig date of this	application:
	or abandoned)	application:
	or abandoned)	application:
	or abandoned)	application:
(status - pending, patented	or abandoned)	application:
o transad	(status - pending, patented of status - pending, patented of status - pending, patented of visional application(s) listed below the state of the sta	ct all business in the Patent and Trademark Office Laurence B. Bond, Reg. No. 30,549 H. Dickson Burton, Reg. No. 30,453 Keil to patentability as defined in Title 37, Code of anational or PCT international filing date of this contained in the patental or abandoned) (status - pending, patented or abandoned) visional application(s) listed below:

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole inventor: James M. Derderian

Inventor's signature

Residence: Boise, Idaho Citizenship: U.S.A.

Post Office Address: 4048 South Iriondo Way, Boise, ID 83706

TRASKBRITT, PC P.O. BOX 2550

Salt Lake City, Utah 84110







IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: James M. Derderian

Examiner:

Unknown Unknown

Serial No.: Not yet assigned

Group Art Unit:

Filed:

Attorney Docket No.: 4830US (01-0106)

Title:

SPACER FOR SEMICONDUCTOR DEVICES, SEMICONDUCTOR DEVICES AND ASSEMBLIES INCLUDING THE

SPACER, AND METHODS

POWER OF ATTORNEY BY ASSIGNEE AND CERTIFICATE UNDER 37 CFR § 3.73(b)

Commissioner for Patents Washington, D.C. 20231

Sir:

MICRON TECHNOLOGY, INC., assignee of the entire right, title and interest by assignment from the inventor(s) in the above-identified application, hereby appoints the following attorneys and agents:

David V. Trask, Reg. No. 22,012 Joseph A. Walkowski, Reg. No. 28,765 Allen C. Turner, Reg. No. 33,041 Brick G. Power, Reg. No. 38,581 Krista Weber Powell, Reg. No. 47,867 Shawn G. Hansen, Reg. No. 42,627 Katherine A. Hamer, Reg. No. 47,628

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Laurence B. Bond, Reg. No. 30,549 H. Dickson Burton, Reg. No. P-48,396 Kent S. Burningham, Reg. No. 30,453 Devin R. Jensen, Reg. No. 44,805 David L. Stott, Reg. No. 43,937 Bradley B. Jensen, Reg. No. 46,801 Charles B. Brantley II, Reg. No. 38,086

as its attorneys with full power of substitution to prosecute this application and all applications claiming filing date priority therefrom and to transact all business in the U.S. Patent and Trademark Office in connection therewith.

The above-identified assignee hereby elects, pursuant to 37 C.F.R. § 3.71, to conduct the prosecution of the above-identified patent application to the exclusion of the inventor(s).

A chain of title from the inventor(s) of the above-identified patent application to the above-identified assignee is shown:

- [] In an assignment recorded in the U.S. Patent and Trademark Office at Reel, Frame.
- [x] In an assignment filed herewith for recordation, a true copy of which is attached hereto.

The undersigned has reviewed the above-identified assignment and, to the best of his knowledge and belief, title is in the above-identified assignee.

The undersigned further avers that he is empowered to make and sign the foregoing certification on behalf of the above-identified assignee, and to take the action set forth herein on its behalf.

Please direct all communications regarding the above-identified application to:

Brick G. Power, TRASKBRITT, PC P.O. Box 2550 Salt Lake City, UT 84110 Tele: (801) 532-1922 Fax: (801) 531-9168

Respectfully Submitted,

MICRON TECHNOLOGY, INC.

Michael L. Lynch, Esq. Reg. No. 30,871

Chief Patent Counsel, MICRON TECHNOLOGY, INC.

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